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Substitute for Form 1449A/PTO		<b>Complete If Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		<i>(Use as many sheets as necessary)</i>	
Sheet	1	of	1
		<i>Application Number</i>	10/647,688
		<i>Filing Date</i>	August 25, 2003
		<i>First Named Inventor</i>	S. Brandon Keller
		<i>Art Unit</i>	2805
		<i>Examiner Name</i>	Whitmore
		<i>Attorney Docket Number</i>	100111238-1

## **U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
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**\*EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

## PATENT APPLICATION

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<p style="text-align: center;">O P E FEB 06 2004 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)</p>	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	100111238-1	10/647,688	
	APPLICANT	S. Brandon Keller, et al.	
	FILING DATE	GROUP Aug. 25, 2003	

## REFERENCE DESIGNATION

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EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
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## FOREIGN PATENT DOCUMENTS

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## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,682, Entitled: Method And Program Product For Determining Nets Requiring Detailed Electromigration And Self Heating Analysis In A Digital Integrated Circuit; Filed November 12, 2003
1R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,698, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003
1S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,376, Entitled: Method And Program For Visual Display and One-Click Repair Of Electromigration And Self Heating Design-Rule Violations In A Digital Integrated Circuit Layout Database; Filed November 12, 2003

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## PATENT APPLICATION

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LIST OF PATENTS AND PUBLICATIONS FOR  
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10/647,688

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APPLICANT

S. Brandon Keller, et al.

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2Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,501, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit By Converting A Netlist To A DC Model And Performing DC Analysis Of The DC Model; Filed November 12, 2003
2R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,526, Entitled: Method And Program Product For Performing A Worst Case Electromigration And Self Heating Analysis In A Digital Integrated Circuit With Worst-Case Superposition Of Partial Currents; Filed November 12, 2003
2S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,692, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003

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LIST OF PATENTS AND PUBLICATIONS FOR  
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100111238-1	10/647,688	
APPLICANT		
S. Brandon Keller, et al.		
FILING DATE	GROUP	
Aug. 25, 2003	08/05	

## REFERENCE DESIGNATION

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3Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,508, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Layout Database by Substituting Resistive Models For Active Devices; Filed November 12, 2003
3R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655516US; Entitled: Method And Program Product For Determining Worst Case Currents In A Digital Integrated Circuit Through Worst-Case Superposition Of Partial Currents; Docket No.: 200300123-1; Filed January 30, 2004 <i>101769,675</i>
3S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655564US; Docket: 200308901-1; Entitled: Systems And Methods For Re-Using Circuit Design Analysis Results; Filed January 30, 2004 <i>101769,702</i>

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FORM PTO-1449  LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT  (Use several sheets if necessary)	ATTY. DOCKET NO. 100111238-1	APPLICATION NO. 10/647,688	CONFIRMATION NO.
	APPLICANT S. Brandon Keller, et al.	FILING DATE Aug. 25, 2003	GROUP D826

REFERENCE DESIGNATION

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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

4Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655581US; Docket No.: 200308899-1; Entitled: System And Method For Determining Detail Of Analysis In A Circuit Design; Filed January 30, 2004  10/769,687
4R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655595US; Docket No.: 200308898-1; Entitled: Systems And Methods That Identify Equivalent Instantiation-Specific Configuration Information For Analysis Tools; Filed January 30, 2004  10/769,495
4S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H. & STEVENS, Scott Alan; U.S. Patent Application filed under EV210655520US; Entitled: System And Method To Limit Analyzed Current Flow In A Circuit Design; Filed January 30, 2004  10/789,682

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ATTY. DOCKET NO.  
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10/647,688

CONFIRMATION NO.

APPLICANT

S. Brandon Keller, et al.

FILING DATE

Aug. 25, 2003

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## REFERENCE DESIGNATION

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5Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655533US; Docket: 200310172-1; Entitled: System And Method For Processing Configuration Information; Filed January 30, 2004  101768,442
5R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655555US; Docket: 200310170-1; Entitled: System And Method For Balancing Run-Time And Result Accuracy In A Circuit Design Analysis Tool; Filed January 30, 2004  101769,683
5S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655578US; Docket: 200308900-1; Entitled: System And Method For Indicating Logic State Combinations Used During Circuit Design Analysis; Filed January 30, 2004  101769,676

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## PATENT APPLICATION

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ATTY. DOCKET NO. 100111238-1	APPLICATION NO. 10/647,688	CONFIRMATION NO.
APPLICANT <b>S. Brandon Keller, et al.</b>		
FILING DATE Aug. 25, 2003	GROUP <i>1816</i>	

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## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

6Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655547US; Docket: 200310171-1; Entitled: System And Method For Determining Control Signal Combinations For Use During Simulation Of A Stage Of A Circuit Design; Filed January 30, 2004 <i>101769, 673</i>
6R	
6S	

EXAMINER

DATE CONSIDERED

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